REMARKS

Claims 1-5, 7-16, 18-21 are pending in the present application. Claims 6 and 17 are cancelled herein. Claims 1, 7, 8, 10, 12, and 18 have been amended. No new matter has been added. Applicant respectfully requests reconsideration of the claims in view of the following remarks.

Claims 1-4, 6, 8-15, 17 and 20-21 have been rejected under 35 U.S.C. § 103(a) as being unpatentable over Kurjanowicz et al. (U.S. Patent No. 6,894,941 B2, hereinafter "Kurjanowicz"), in view of a Feurle et al. (U.S. Patent Application No. 2003/0043674 A1, hereinafter "Feurle"). This rejection is hereby respectfully traversed.

With respect to independent claims 1 and 12, the Examiner remarked that Kurjanowicz teaches a method of reducing a rate for refreshing a portion of a dynamic access memory, comprising providing a first portion of said DRAM comprising a plurality of volatile memory cells (here the Examiner cited Col. 7, lines 26-38 and Col. 8, lines 33-36 and memory banks 102 and 108) permitting refresh at a first rate (here the Examiner cited the single cell per bit mode, Col. 15, lines 37-39), and a second portion of said DRAM (here the Examiner cited upper memory bank 102 when the DRAM of Kurjanowicz operates in a short page access mode, and cited Col. 7 lines 26-45 and Col. 8 lines 33-36) comprising a plurality of volatile memory cells,